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Docket No.: END920030035US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Austin et al.

Group Art Unit: 2816 : IBM Corporation
Examiner: Margret R. Wambach : Intellectual Property Law
Serial No.: 10/661,050 : Dept. IQ0A, Bldg. 040-3
Filed: 09/11/2003 : 1701 North Street
Title: PROGRAMMABLE LOW-POWER : Endicott, NY 13760
HIGH-FREQUENCY DIVIDER

U.S. Patent No.: 6,917,662 B2

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on
6/26/06
Georgia Y. Brundage 6/26/06
Georgia Y. Brundage Date

DISCLOSURE AFTER PATENT IS GRANTED

Dear Sir:

Pursuant to 37 CFR 1.501, patentee of U.S. Patent No. 6,917,662 B2 (S.N. 10/661,050 filed on 09/11/03) desires to have the following published documents made of record in the patent file of the above referenced issued patent:

<u>Japanese Patent Documents No(s).</u>	<u>Publication Date(s)</u>
PUPA 11-154710	06/08/99
PUPA 61-198817	09/03/86
PUPA 2000-209082	07/28/00
PUPA 2000-509222(corresponding to abstract WO 97/40497)	07/18/00
PUPA 2002-181946	06/30/00
PUPA 11-111940	04/23/99
PUPA 10-111674	04/28/98

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U.S. Patent Document No(s).

U.S. 5,917,355

Publication Date(s)

06/29/99

Other Documents

W.N. Carr et al., "MOS/LSI Design and Application", McGraw-Hill Corp., pp77, pp126, 1972.

Copies of the non-US published documents with English abstracts are enclosed herein for inclusion in the patent file.

Please charge any associated fees to our Deposit Account 09-0457. A copy of this paper is enclosed.

Respectfully submitted,

Dated: 6/26/06

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